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FIG. 1

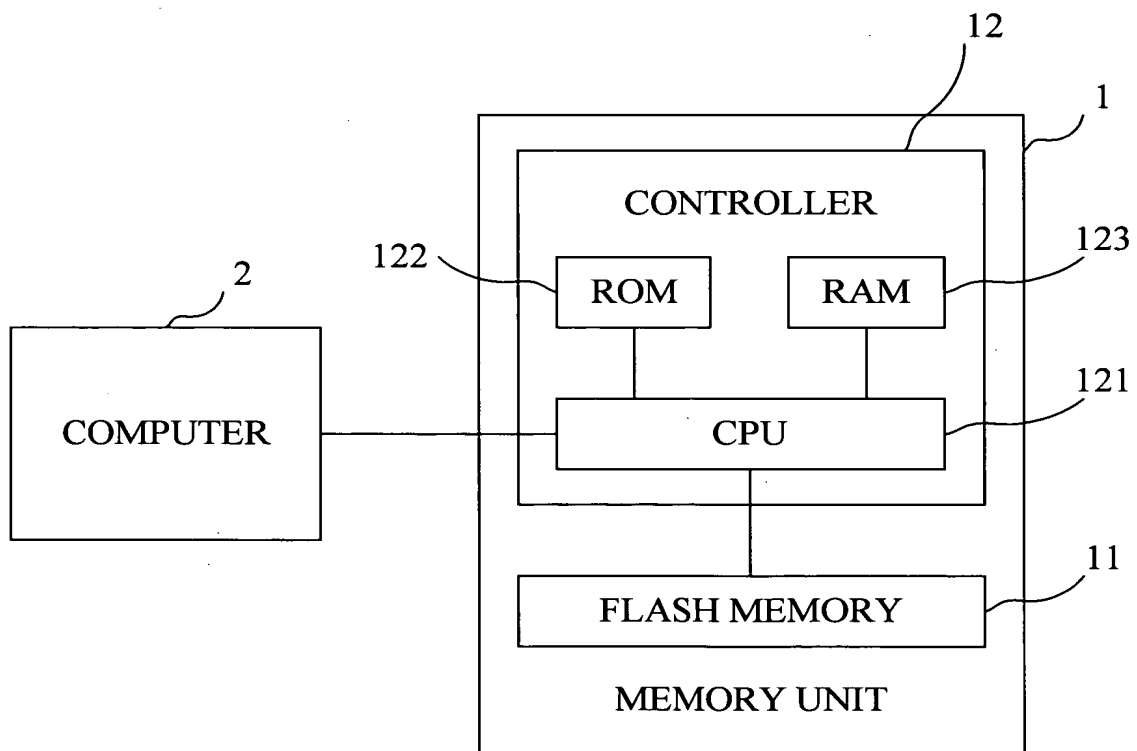
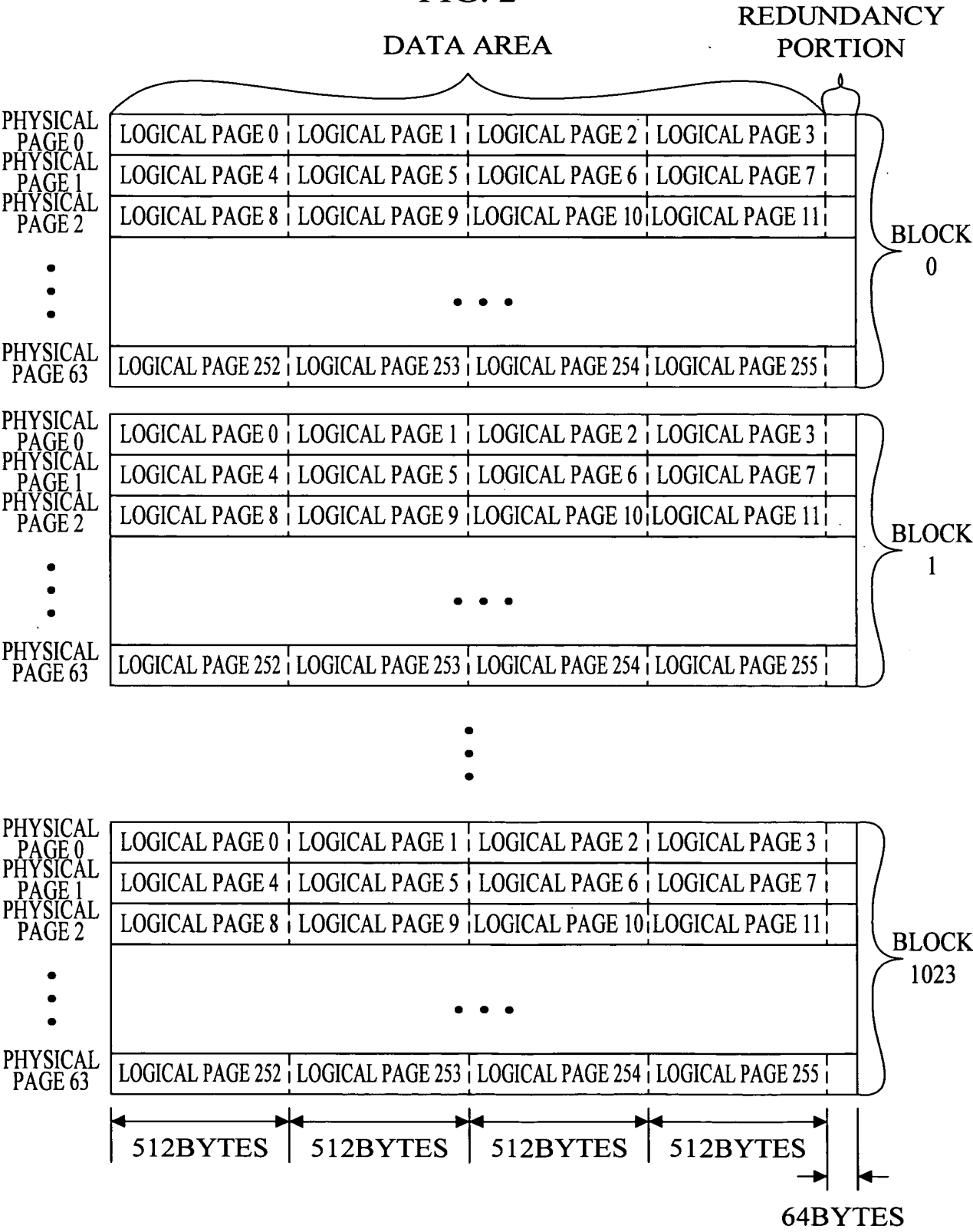
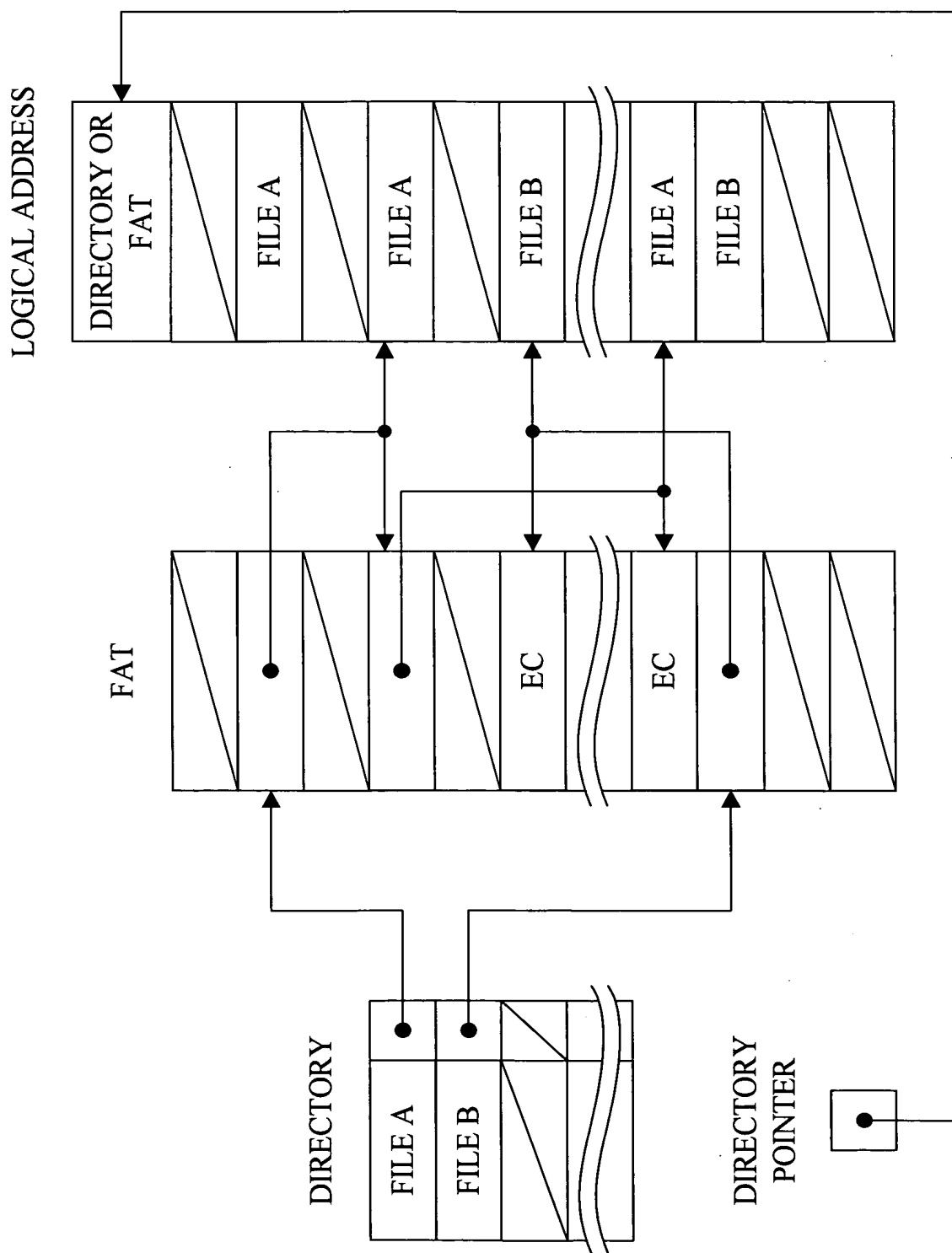


FIG. 2



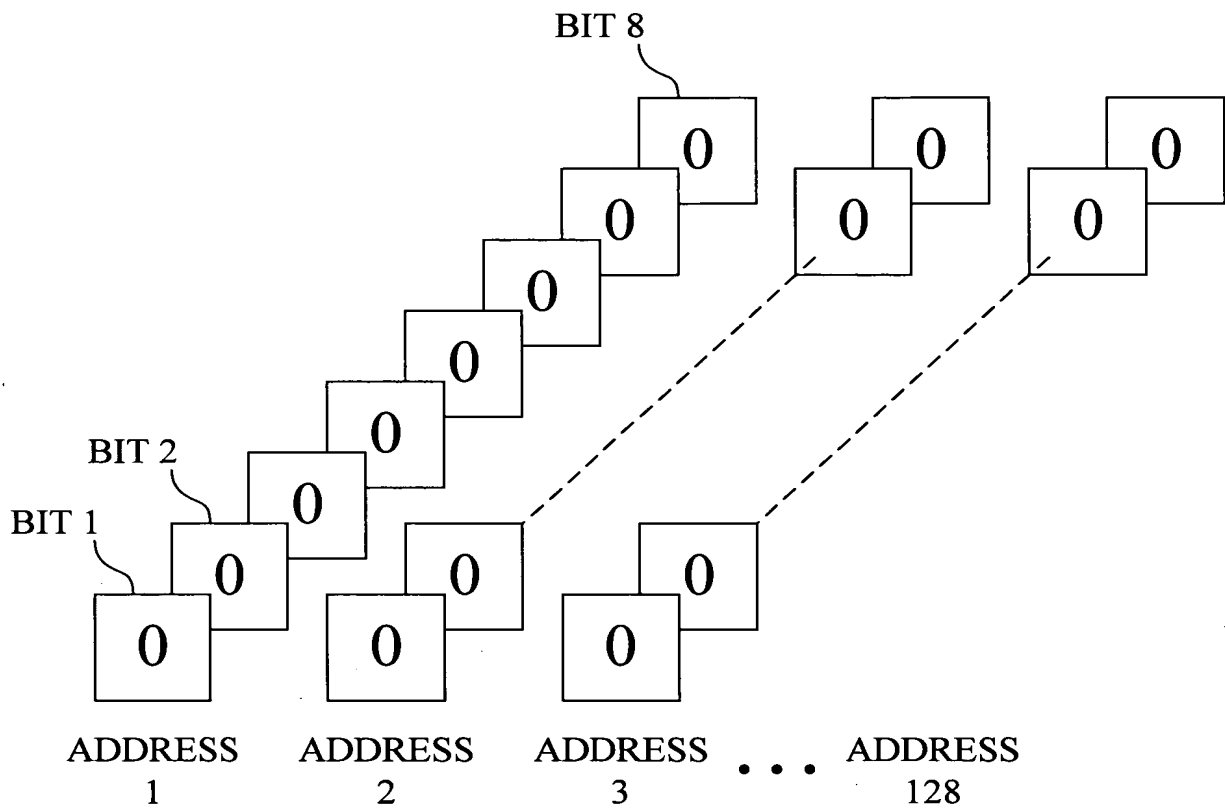
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FIG. 3



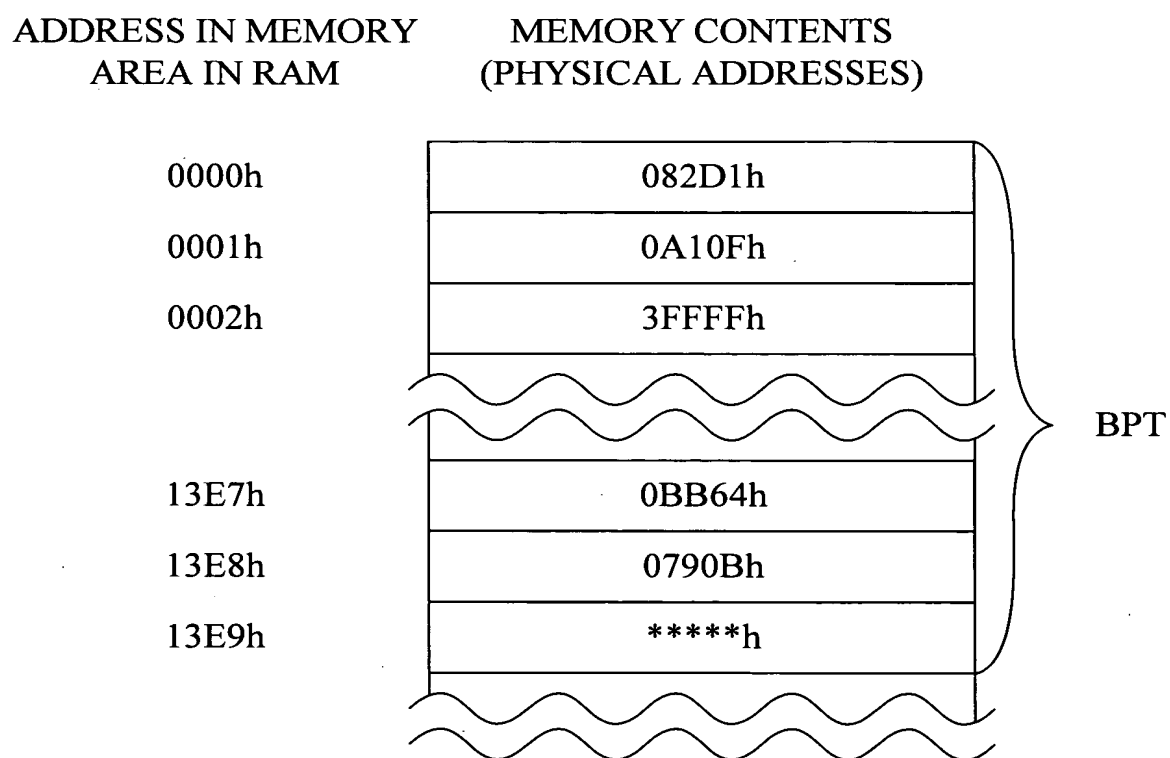
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FIG. 4

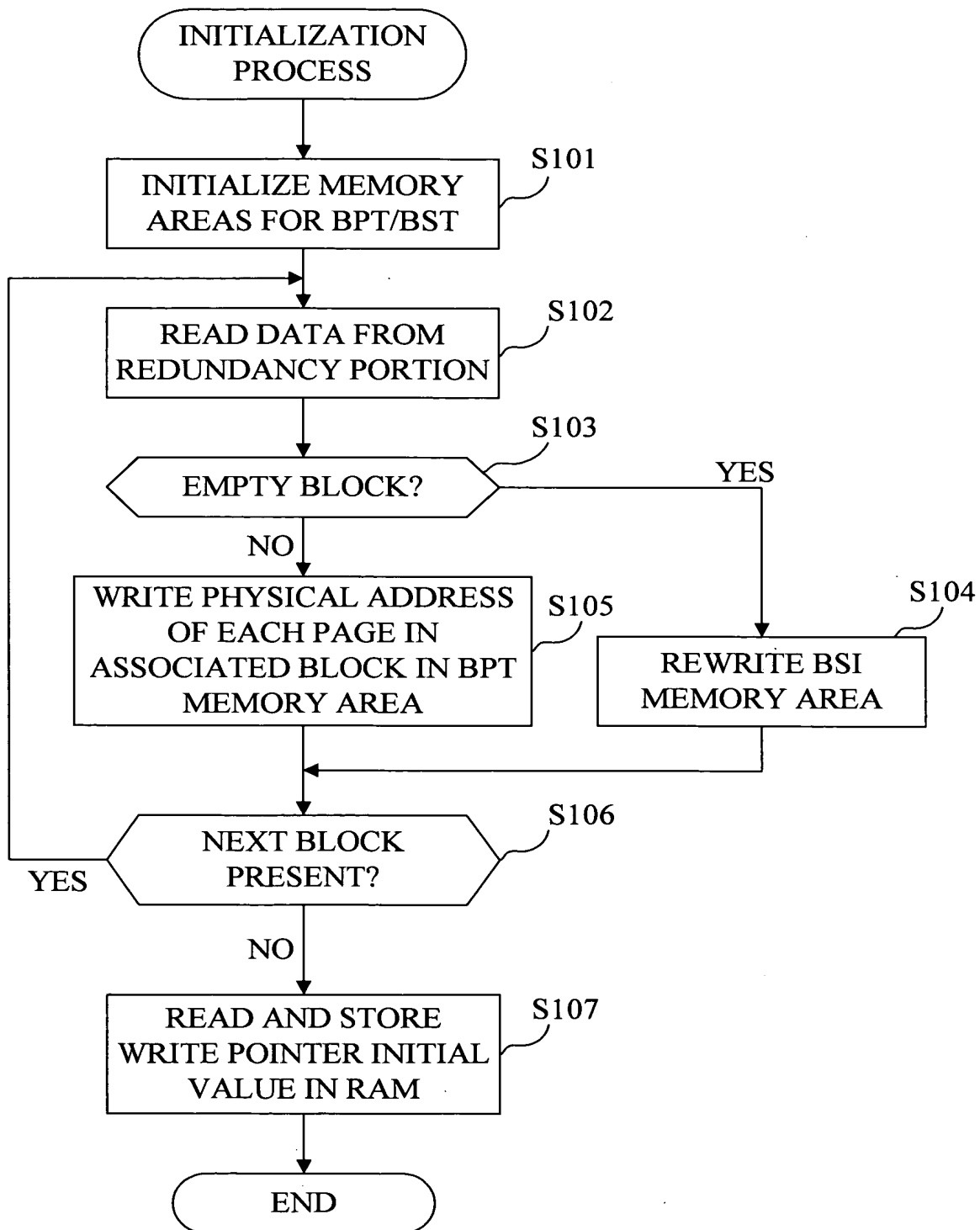


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FIG. 5



(ADDRESS IN MEMORY AREA IN RAM TO STORE BPT)  
= (LOGICAL ADDRESS) - OFFSET VALUE

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FIG. 6

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FIG. 7

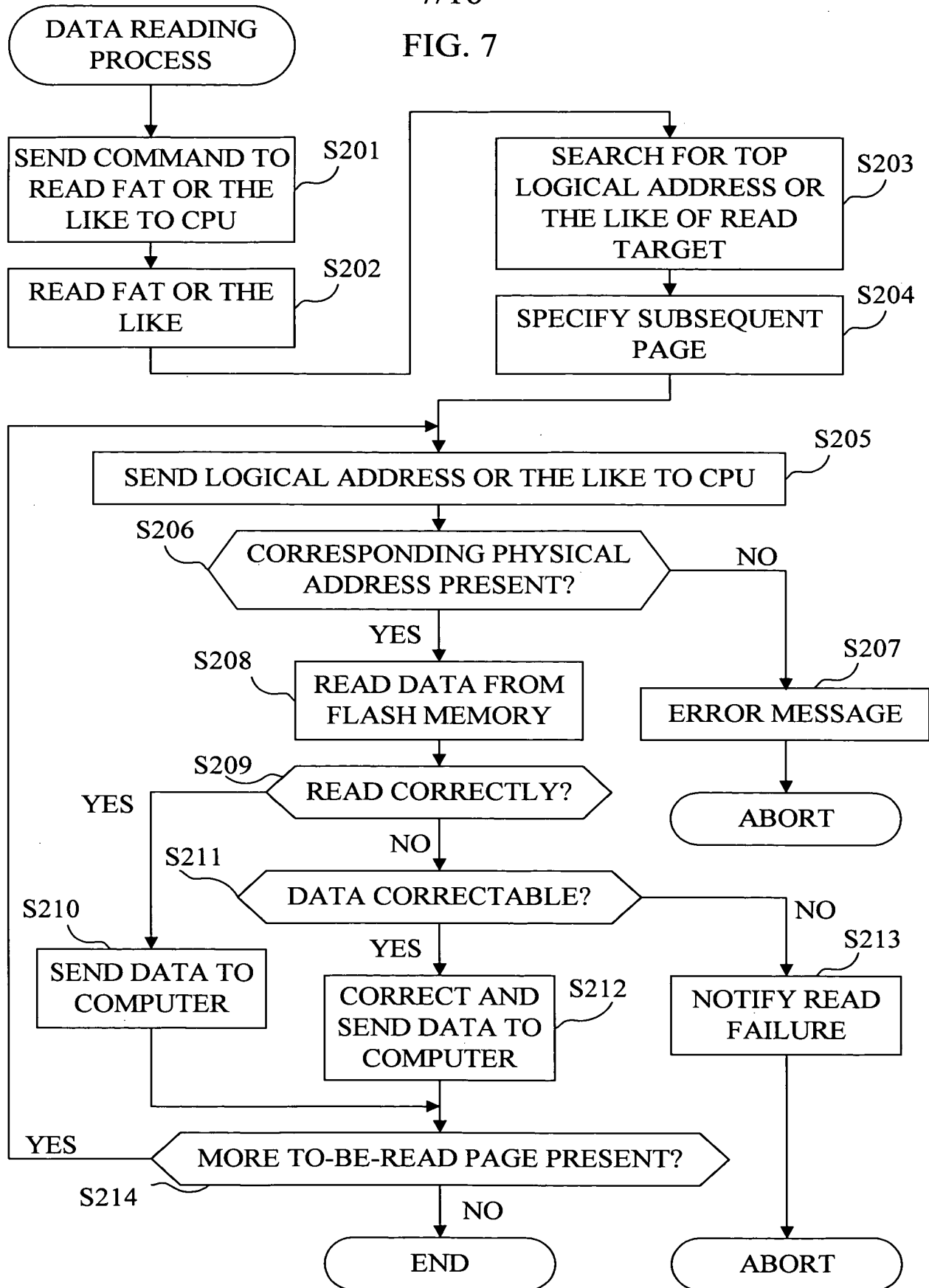
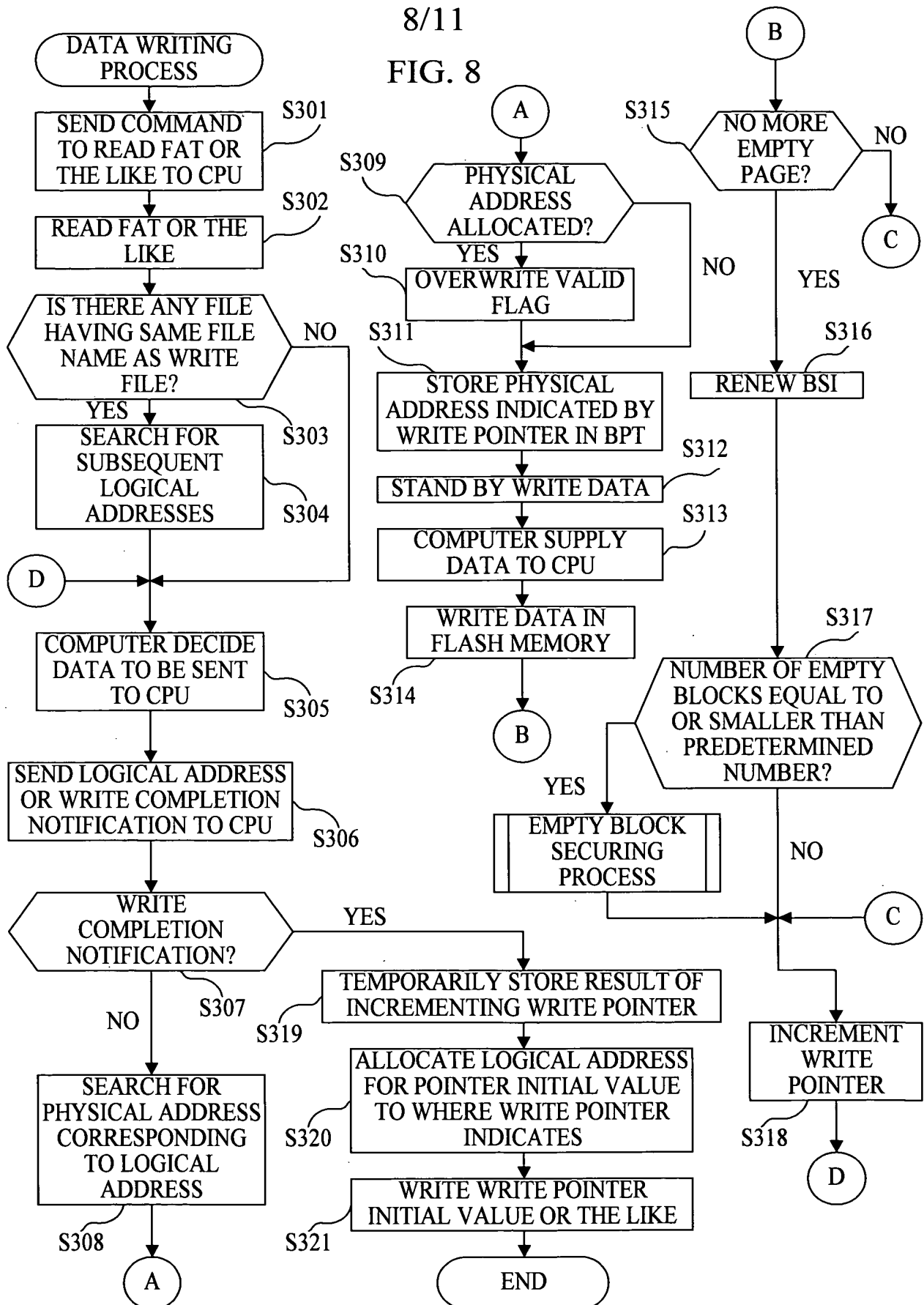


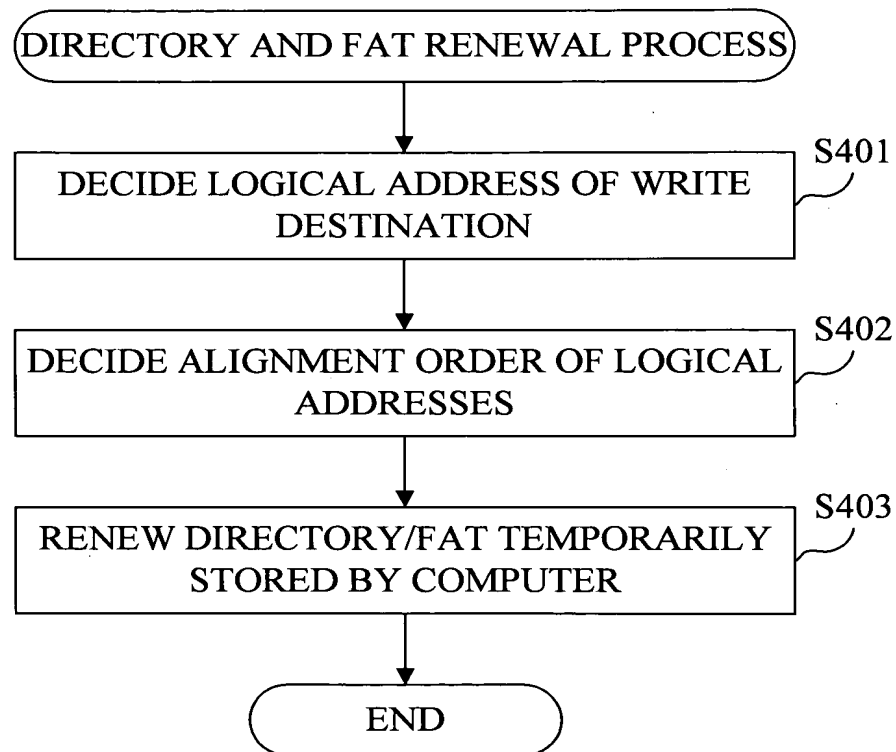
FIG. 8





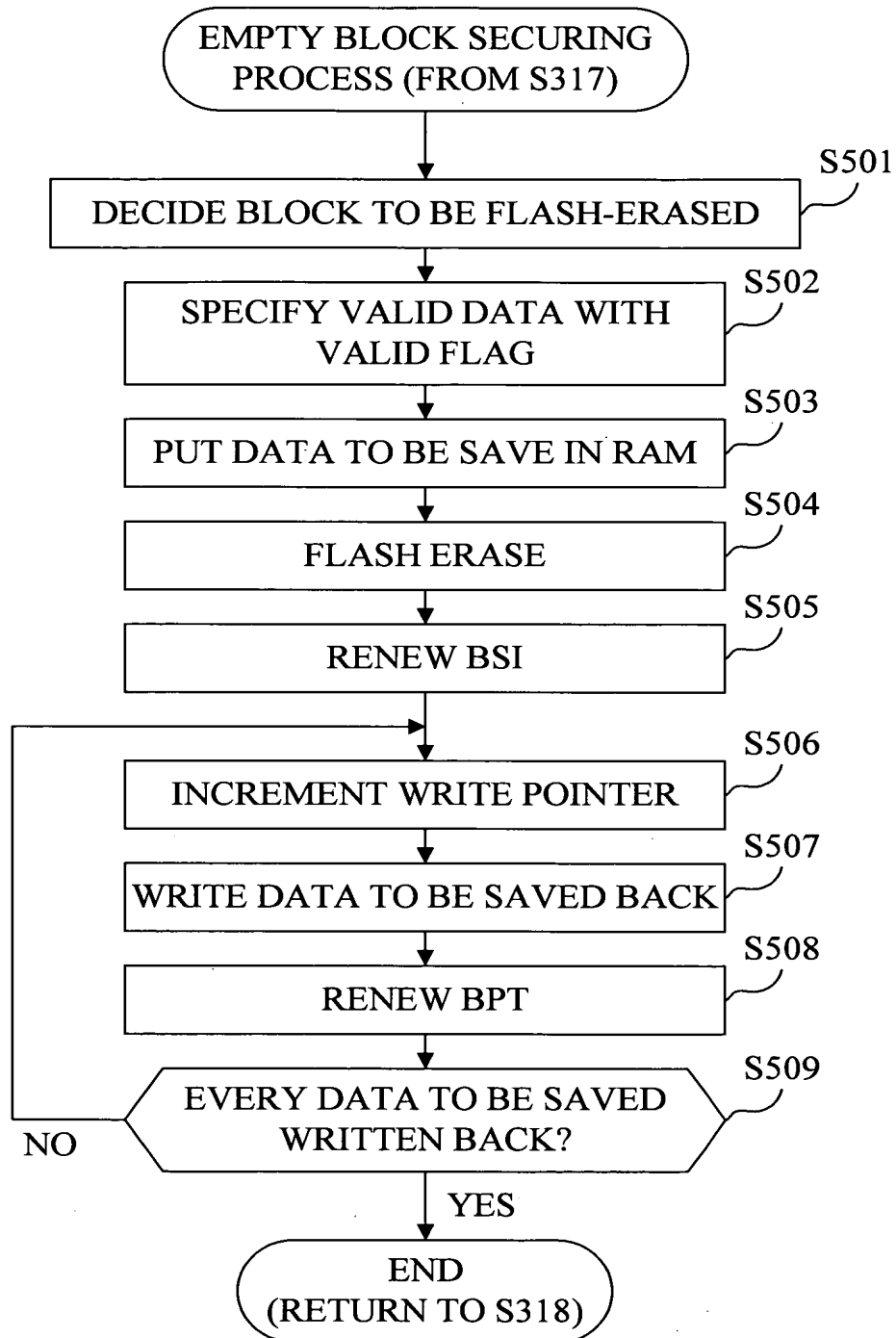
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FIG. 9



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FIG. 10



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FIG. 11

